- KAMAYA	онм ———		
	•••••	Spec. No.: Date:	RBX–K–HTS-0001 /2 2023. 1. 20
	Data s	heet	
	KED THICK FILM CHIP R ID HIGH POWER · ANTI SU		
Style: RE	BX16,20,32,35		
	AEC-Q200 q	ualified	
	RoHS COMPLI Halogen and Ar		
	 Stock conditions Temperature: +5°C ~ +35°C Relative humidity: 25% ~ 75% The period of guarantee: Within Solder Product specification contained time without notice If you have any questions or a agreement is necessary, plear 	rability shall be satisfi ed in this data shee a Purchasing Specif	ed. It are subject to change at any fication for any quality

No: RBX-K-HTS-0001 /2

Title: FIXED THICK FILM CHIP RESISTORS; RECTANGULAR TYPE AND HIGH POWER · ANTI SURGE · ANTI-SULFURATION RBX16,20,32,35

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1. Scope

1.1 This data sheet covers the detail requirements for fixed thick film chip resistors; rectangular type & high power · anti surge, style of RBX16,20,32,35.

1.2 Applicable documents

JIS C 5201-1: 2011, IEC60115-1: 2008, AEC-Q200 Rev.D

2. Classification

(Example)

Type designation shall be the following form.

RBX	16	K	123	J	TP
1	2	3	4	5	6
•					

Style

1 Fixed thick film chip resistors; rectangular type & & high power · anti surge

2 Rated dissipation and / or dimension

Style

3 Temperature coefficient of resistance

K	±100×10 ⁻⁶ / °C
–(Dash)	Standard

4 Rated resistance

123	E24 Series, 3 digit,	Ex. 123> 12kΩ,
1000	E96 Series, 4 digit,	Ex. 1000>100Ω
	_	1022> 10.2kΩ

5 Tolerance on rated resistance

D	±0.5%
F	±1%
J	±5%

6 Packaging form

0 0	
В	Bulk (loose package)
TP	Paper taping
TE	Embossed taping

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3. Rating

3.1 The ratings shall be in accordance with Table-1.

	Table-1					
Style	Rated dissipation (W)		ture coefficient of nce (10 ⁶ /°C)	Rated resistance range (Ω)	Preferred number series for resistors	Tolerance on rated resistance
		K	±100	10~1M	E24, 96	D(±0.5%),
RBX16	0.25	Standard	±200	1.0~9.76	L24, 50	F(±1%)
TIDATO	0.25	K	±100	10~1M	E24	1/150/)
		Standard	±200	1.0~9.1	L24	J(±5%)
		K	±100	10~1M	E24, 96	D(±0.5%),
DRV00	RBX20 0.33	Standard	±200	1.0~9.76		F(±1%)
		K	±100	10~1M	E24	J(±5%)
		Standard	±200	1.0~9.1		
		K	±100	10~1M	E24, 96	D(±0.5%), F(±1%)
RBX32	0.5	Standard	±200	1.0~9.76	⊏24, 90	
RDA32	0.5	K	±100	10~1M	F04	
		Standard	±200	1.0~9.1	E24	J(±5%)
		K	±100	10~1M	E24.06	D(±0.5%),
DDV05	0.75	Standard	±200	1.0~9.76	E24, 96	F(±1%)
RBX35	0.75	K	±100	10~1M	F04	1/+59/)
		Standard	±200	1.0~9.1	E24	J(±5%)

Style	Limiting element voltage (V)	Insulation voltage (V)	Category temperature range(°C)
RBX16	150	150	
RBX20			55. · 155
RBX32	200	500	-55~+155
RBX35			

3.2 Derating

The derated values of dissipation at temperature in excess of 70 °C shall be as indicated by the following curve.

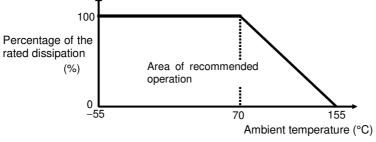


Figure-1 Derating curve

3.3 Rated voltage

d.c.or a.c.r.m.s.voltage calculated from the square root of the product of the rated resistance and the rated dissipation.

$$E = \sqrt{P \cdot R}$$

Limiting element voltage can only be applied to resistors when the resistance value is equal to or higher than the critical resistance value.

At high value of resistance, the rated voltage may not be applicable.

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Title: FIXED THICK FILM CHIP RESISTORS; RECTANGULAR TYPE AND HIGH POWER · ANTI SURGE - ANTI-SULFURATION RBX16,20,32,35

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4. Packaging form

The standard packaging form shall be in accordance with Table-2.

Table-2

Symbol	Packaging form		Standard packaging quantity / units	Application
В	Bulk (loose package)		1,000 pcs.	RBX16,20,32,35
TP	Paper taping	8mm width, 4mm pitches	5,000 pcs.	RBX16,20,32
TE	Embossed taping	8mm width, 4mm pitches	4,000 pcs.	RBX35

5. Dimensions

5.1 The resistor shall be of the design and physical dimensions in accordance with Figure-2 and Table-3.

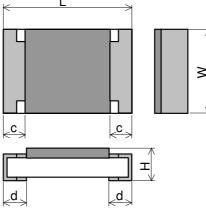


Figure-2

	Table–3			Unit: mm	
Style	L	W	Н	С	d
RBX16	1.6±0.1	0.8 +0.15	0.45±0.10	0.25±0.10	0.3±0.1
RBX20	2.0±0.1	1.25±0.10	0.55±0.10	0.3±0.2	0.4±0.2
RBX32	3.1±0.1	1.6±0.15	0.55±0.10	0.4±0.25	0.5±0.25
RBX35	3.1±0.15	2.5±0.15	0.55±0.15	0.4±0.25	0.5±0.25

5.2 Net weight (Reference)

Style	Net weight(mg)
RBX16	2
RBX20	5
RBX32	9
RBX35	16

6. Marking

The nominal resistance shall be marked in 3 digits or 4 digits and marked on over coat side.

• E24 series: 3 digits, E96 series: 4 digits

In case of the resistance value that E96 overlaps with E24, It is marked by either.

The Rated resistance of RBX16 should not be marked in 4 digits (E96).

		J ()
Marking example	Contents	Application
123	$12 \times 10^3 \ [\Omega] \rightarrow 12 \ [k\Omega]$	RBX16,20,32,35
2R2	2.2 [Ω]	Less than 10Ω of RBX16,20,32,35
5623	$562 \times 10^{3} [\Omega] \rightarrow 562[k\Omega]$	RBX20,32,35
12R7	12.7 [Ω]	RBX20,32,35

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7. Performance

7.1 The standard condition for tests shall be in accordance with Sub-clause 4.2, JIS C 5201-1: 2011.

7.2 The performance shall be satisfied in Table-4.

, <u> </u>	ne performance shall be salish	Table-4(1)		
No.	Test items	Condition of test	Performance requirements	
1	High temperature exposure AEC Q200 - No.3	MIL-STD-202 Method 108 Ambient temperature:155±2°C, Condition: Without load, Duration: 1000 ⁺⁴⁸ / ₀ h	Resistor: $\Delta R/R$: Within $\pm(1\%+0.05\Omega)$ No visible damage	
2	Temperature cycling	Interval measurements: 250 h and 500 h JESD22 Method JA-104	Resistor: $\Delta R/R$: Within ±(0.5%+0.05 Ω)	
	AEC Q200 - No.4	Temperature: -55±3°C / 125±2°C, Dwell time: 30min maximum at each temp.No visible damageTransition time: 1 min. max. Number of cycles: 1000 cycles. Interval measurements: 250 cy and 500 cyNo visible damage		
3	Bias humidity AEC Q200 – No.7	 MIL-STD-202 Method 103 Condition: 85°C & 85% R.H. Test power: 10% of rated power shall be applied for continuously. Duration: 1,000 ⁺⁴⁸/₀ h Interval measurements: 250 h and 500 h 	Resistor: $\Delta R/R$: Within ±(1%+0.05 Ω) No visible damage	
4	Operational life AEC Q200 – No.8	MIL-STD-202 Method 108 Ambient temperature: $125\pm2^{\circ}$ C The applied voltage shall be the voltage to be calculated at 35% of rated dissipation or the limiting element voltage whichever is the smaller. Condition: The voltage shall be applied for continuously. Duration: $1000 {}^{+48}_{0} h$ Interval measurements: 250 h and 500 h	Resistor: ΔR/R: Within ±(1%+0.05Ω) No visible damage	
5	Dimensions AEC Q200 – No.10	JESD22 Method JB-100	As in Table-3	
6	Resistance to Solvents AEC Q200 – No.12	MIL-STD-202 Method 215 Solvent: 2–propanol at 25°C Immersion time: 3 min Brush: 10 times brushing Immersion and brush cycle: 3cycle	Resistor: $\Delta R/R$: Within ±(0.5%+0.05 Ω) No visible damage	
7	Mechanical Shock AEC Q200 – No.13	MIL-STD-202 Method 213 Waveform: half sine, Peak value100G, Normal duration 6ms Condition: XX'YY'ZZ', 10times each	Resistor: $\Delta R/R$: Within $\pm (0.5\%+0.05\Omega)$ No visible damage	

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Table-4(2)								
No	Test items	Condition of test	Performance requirements					
8	Vibration	MIL-STD-202 Method 204	Resistor: $\Delta R/R$: Within ±(0.5%+0.05 Ω)					
	AEC Q200 – No.14	Peak acceleration and Sweep time: 5 g's for 20	No visible damage					
		min, Frequency 10Hz to 2000Hz,						
-		Condition: 12 cycles each of 3 orientations						
9	Resistance to soldering heat	MIL-STD-202 Method 210	Resistor: Δ R/R: Within ±(0.5%+0.05 Ω)					
	AEC Q200 - No.15	Solder bath temp: 260±5°C	No visible damage					
10	ESD test	Immersed time: 10±1s AEC-Q200-002	Desister AD/D-M/Hairs 1/10/ 0.050					
10	AEC Q200 – No.17	Human body model, 2 Kohm, 150 pF,	Resistor: Δ R/R: Within ±(1%+0.05 Ω)					
	AEC Q200-N0.17	Test voltage: RBX16: 2000V	No visible damage					
		RBX20,32,35: 3000V						
11	Solderability	J-STD-002	The surface of terminal immersed					
	AEC Q200 – No.18	a) Bake the sample for 155 °C dwell time 4h /	shall be min. of 95% covered with a					
		solder dipping 235ºC/ 5s.	new coating of solder.					
		Solder: Sn96.5-Ag3-Cu0.5	5					
		b) Category 3, Solder dipping 215°C/5s.						
		Solder: Sn63Pb37						
		c) Category 3, Solder dipping 260°C/ 7s.						
12	Electrical Characterization	1. D.C. Resistance	1. The resistance value shall					
	AEC Q200 - No.19	2. Temperature Coefficient of Resistance	correspond with the rated					
		+20 °C / +155°C	resistance taking into account the					
			specified tolerance.					
13	Ponding atronath	AEC-Q200-005	2. As in Table-1					
13	Bending strengthAEC-Q200-005AEC Q200 – No.21Bending value2mm		Resistor: Δ R/R: Within ±(0.5%+0.05 Ω)					
	AEC 0200-110.21	Holding time: 60sec.	No visible damage					
14	Adhesion	AEC-Q200-006	Resistor: Δ R/R: Within ±(0.5%+0.05 Ω)					
17	AEC Q200 – No.22	Pressurizing force: RBX16: 10N	No remarkable damage or removal of					
		RBX20,32,35: 17.7N	the terminations					
		Test time: $60\pm1s$.						
15	Humid Sulfur vapor test	ASTM B809	Resistor: $\Delta R/R$: Within $\pm (1\%+0.05\Omega)$					
	(FOS)	Reagent: Sulfur (Saturated vapor)	````					
		Test temp.: 60°C						
		Relative humidity: 95%RH						
		Test period: 1000h						
		Resistance						

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8. Taping

8.1 Applicable documents JIS C 0806–3: 2014, EIAJ ET–7200C: 2010

8.2 Taping dimensions

8.2.1 Paper taping (8mm width, 4mm pitches)

Taping dimensions shall be in accordance with Figure-3 and Table-5.

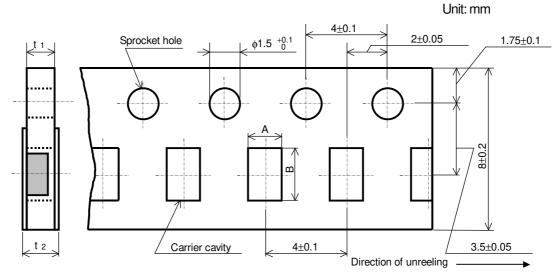
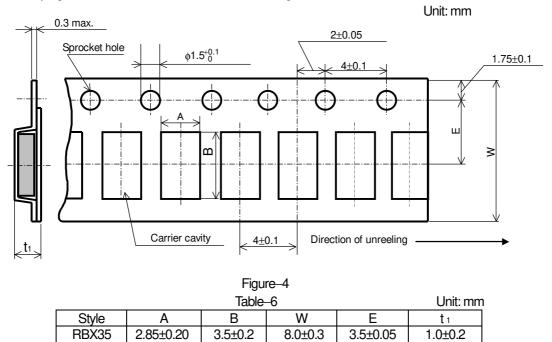


Figure-3

	Unit: mm			
Style	А	В	t 1	t 2
RBX16	1.15 ± 0.15	1.9±0.2	0.6 ± 0.1	0.8max.
RBX20	1.65±0.15	2.5±0.2	0.8±0.1	1.0max.
RBX32	2.00±0.15	3.6±0.2		

8.2.2 Embossed taping dimensions shall be in accordance with Figure-4 and Table-6.



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- 1). The cover tapes shall not cover the sprocket holes.
- 2). Tapes in adjacent layers shall not stick together in the packing.
- 3). Components shall not stick to the carrier tape or to the cover tape.
- 4). Pitch tolerance over any 10 pitches ±0.2mm.
- 5). The peel strength of the top cover tape shall be with in 0.1N to 0.5N on the test method as shown in the following RBX16,20,32: Figure–5, RBX35: Figure–6.
- 6). When the tape is bent with the minimum radius for 25 mm, the tape shall not be damaged and the components shall maintain their position and orientation in the tape.
- 7). In no case shall there be two or more consecutive components missing.
- The maximum number of missing components shall be one or 0.1%, whichever is greater.
- 8). The resistors shall be faced to upward at the over coating side in the carrier cavity.

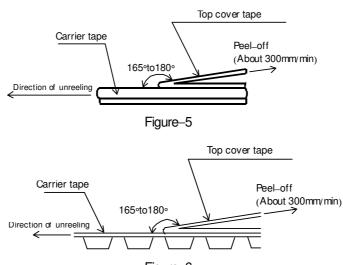


Figure-6

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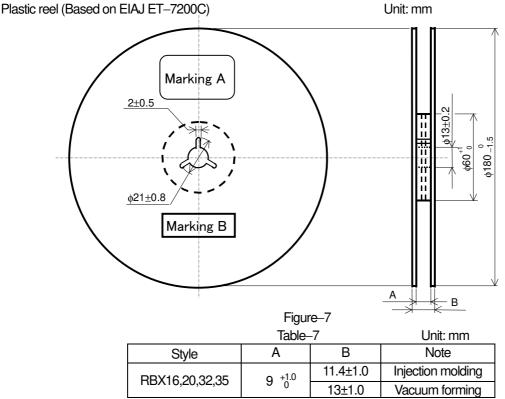
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8.3 Reel dimension

Reel dimensions shall be in accordance with the following Figure–7 and Table–7.



Note: Marking label shall be marked on a place of Marking A or two place of marking A and B.

8.4 Leader and trailer tape.

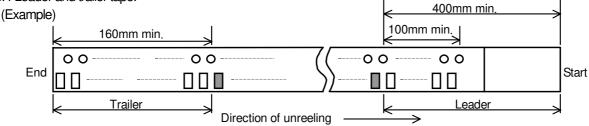


Figure-8

9. Marking on package

The label of a minimum package shall be legibly marked with follows.

9.1 Marking A

(1) Classification

(Style, Temperature coefficient of resistance, Rated resistance, Tolerance on rated resistance, Packaging form)

(2) Quantity (3) Lot number (4) Manufacturer's name or trade mark (5) Others

9.2 Marking B (KAMAYA control label)

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